

FIG. 1



The diagram illustrates a video coding system architecture. An input signal enters from the bottom left and is split into two paths. The first path goes to 'Motion vector detecting means A' (301), and the second path goes to 'Motion vector detecting means B' (313). Both detectors output 'Motion vector A' and 'Motion vector B' respectively. The signal then enters 'Intra/inter judging means' (304), which also receives feedback from 'Predicted image combining means' (315). The output of 304 goes to 'DCT' (305), then 'Quantizing means' (306), and finally 'Variable length code decoding means' (307). A feedback loop labeled 'Intra/inter control signal' connects 307 back to 304. The quantized signal passes through 'Inverse quantizing means' (308) and 'Inverse DCT' (309) before being added to a predicted image at a summing junction (+). The predicted image is selected by switch 312, which is controlled by 'Intra/inter control signal'. The summing junction output goes to 'Frame memory A' (311) and 'Frame memory B' (316). These memories feed into 'Motion compensation means A' (302) and 'Motion compensation means B' (314), which use the motion vectors. Their outputs go to 'Predicted image combining means' (315), which also receives inputs from the memories. The combined predicted image is fed back to the judging means (304) and selected by switch 317 for output.

FIG. 3

1. The first group of people who are interested in the study of the history of the United States are the people who are interested in the history of the United States.

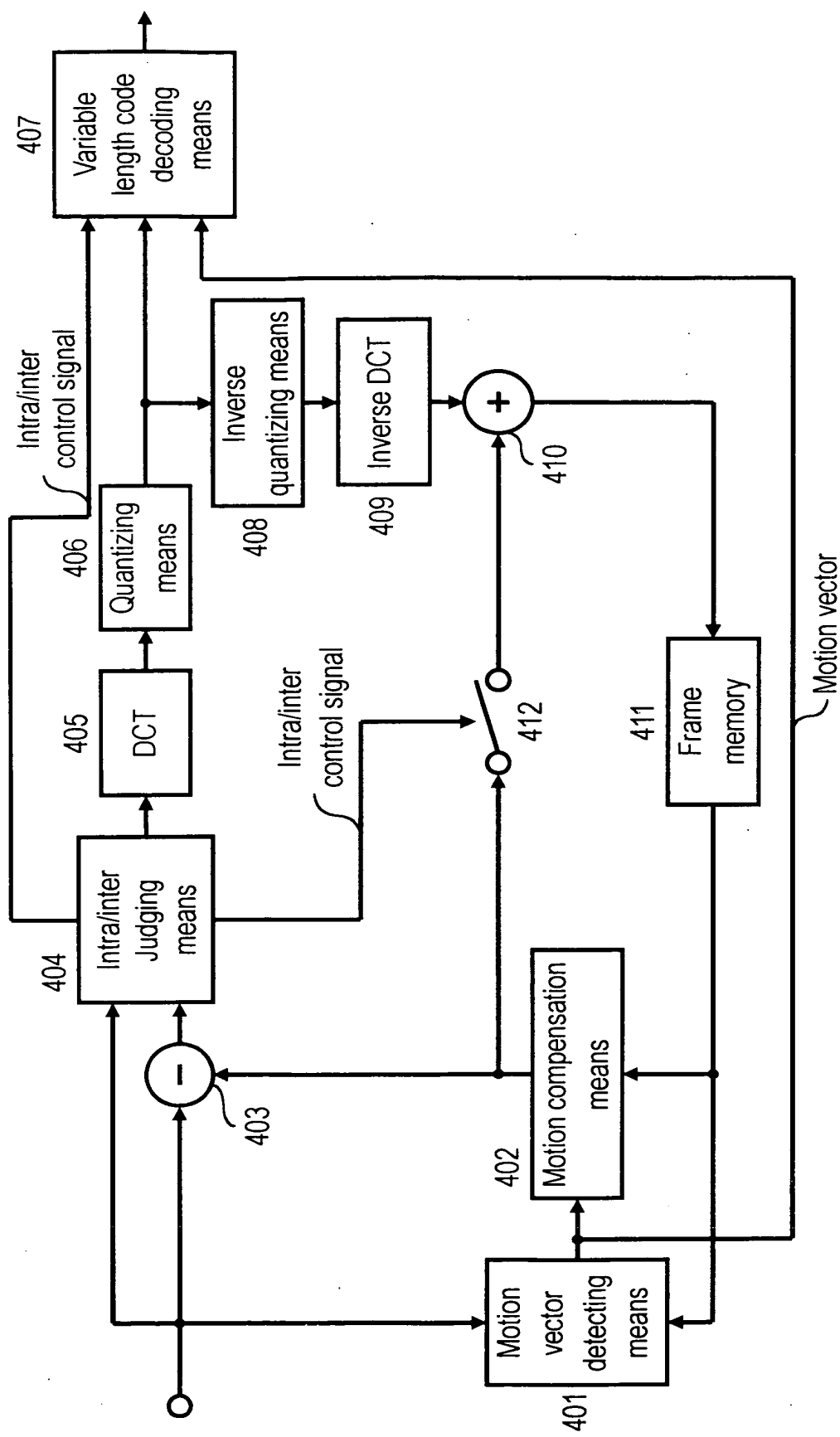


FIG. 4

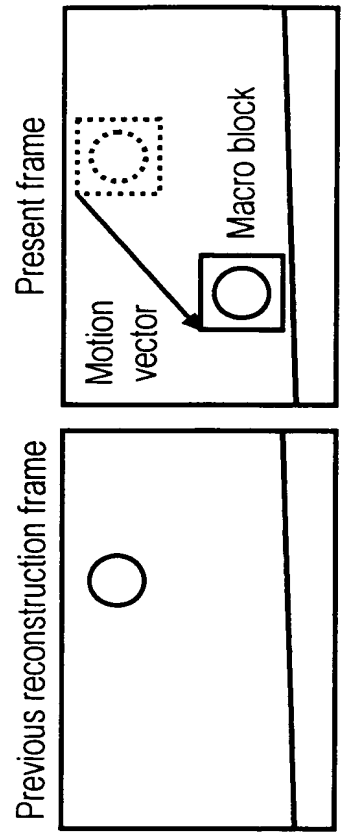


FIG. 5

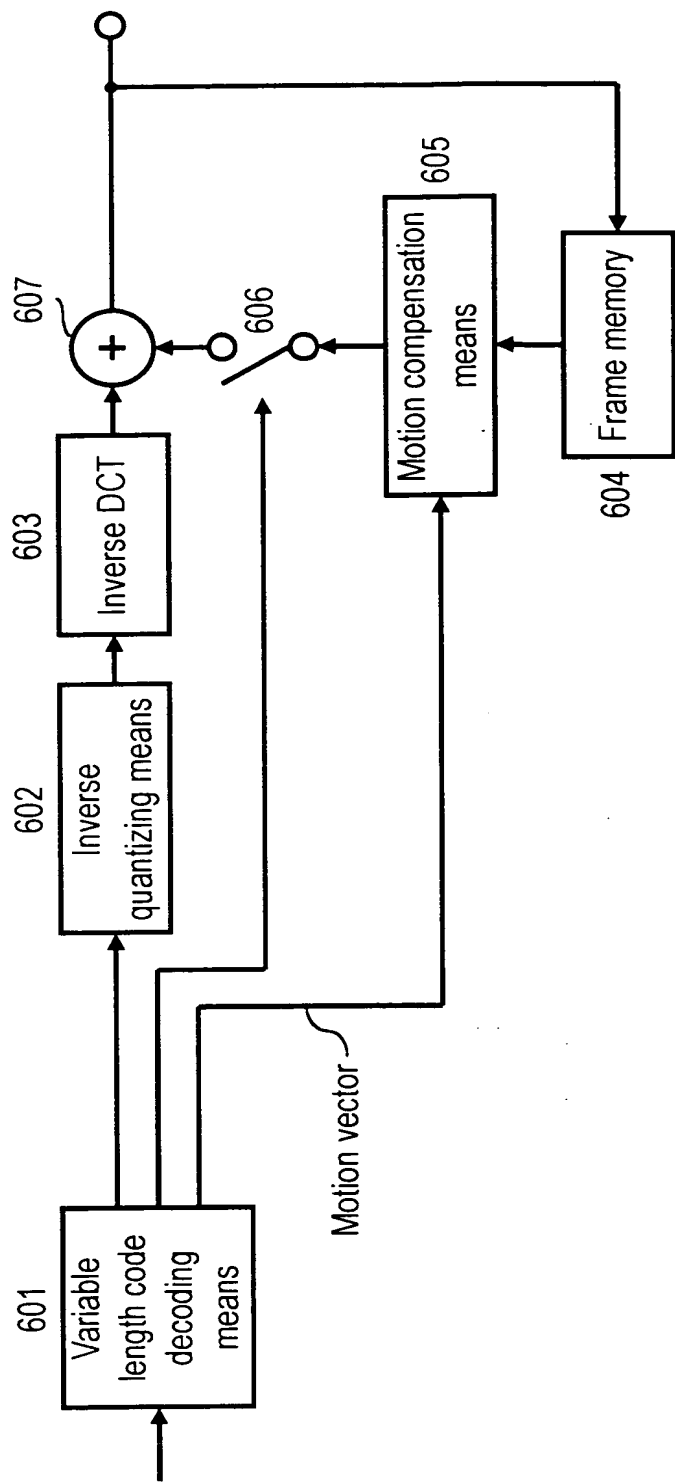


FIG. 6